

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) An electronic circuit comprising:

a plurality of unit circuits;

a first power source line; and

a control circuit that sets a potential of the first power source line to a plurality of potentials or controls an electrical disconnection and an electrical connection between the first power source line and a predetermined voltage,

each of the plurality of unit circuits including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

a second transistor having a third terminal, a fourth terminal, and a second control terminal, the third terminal being coupled to the first control terminal, and the fourth terminal being directly connected to the first power source line;

a capacitive element having a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and

a third transistor having a fifth terminal and a sixth terminal, the third transistor controlling an electrical connection between a data line and the second electrode, and

an electrically conductive state of the first transistor being set during at least a part of a first period in which the data line is electrically connected to the second electrode through the third transistor.

2. (Previously Presented) The electronic circuit according to claim 1, further comprising a second power source line that is coupled to the second terminal.

3. (Previously Presented) The electronic circuit according to claim 1, the second control terminal being coupled to the third terminal.
4. (Previously Presented) The electronic circuit according to claim 1, each unit circuit of the plurality of unit circuits not including any transistor other than the first transistor, the second transistor, and the third transistor.
5. (Previously Presented) The electronic circuit according to claim 1, a conduction type of the first transistor being identical with a conduction type of the second transistor.
6. (Previously Presented) The electronic circuit according to claim 1, each of the plurality of unit circuits further including an electronic element coupled to the first terminal.
7. (Previously Presented) The electronic circuit according to claim 6, the electronic element being a current-driven element.
8. (Previously Presented) The electronic circuit according to claim 1, the control circuit being a fourth transistor having a seventh terminal and an eighth terminal.
9. (Previously Presented) The electronic circuit according to claim 2, the second power source line being set to the predetermined voltage.
10. (Previously Presented) The electronic circuit according to claim 1, a threshold voltage of the first transistor not being lower than a threshold voltage of the second transistor.
11. (Previously Presented) An electronic circuit comprising:
 - a plurality of first signal lines;
 - a plurality of second signal lines;
 - a plurality of first power source lines;

a control circuit that sets a potential of each of the plurality of first power source lines to a plurality of potentials or controls an electrical disconnection and an electrical connection between one first power source line of the plurality of first power source lines and a predetermined voltage; and

a plurality of unit circuits, each of the plurality of unit circuits including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

a second transistor having a third terminal, a fourth terminal, and a second control terminal, the third terminal being coupled to the first control terminal, and the fourth terminal being directly connected to the first power source line;

a capacitive element having a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and

a third transistor having a fifth terminal, a sixth terminal, and a third control terminal, the third transistor controlling an electrical connection between the second electrode and one second signal line of the plurality of second signal lines, the third control terminal being coupled to one first signal line of the plurality of first signal lines, and

an electrically conductive state of the first transistor being determined during at least a part of a period in which the one second signal line is electrically connected to the second electrode through the third transistor.

12. (Previously Presented) The electronic circuit according to claim 11, further comprising a plurality of second power source lines one of which is coupled to the second terminal.

13. (Previously Presented) The electronic circuit according to claim 11, a conduction type of the first transistor being identical with a conduction type of the second transistor.

14. (Previously Presented) The electronic circuit according to claim 11, each of the plurality of unit circuits further including an electronic element that is coupled to the first terminal.

15. (Previously Presented) An electronic circuit comprising:

- a first signal line;
- a second signal line;
- a power source line; and

a plurality of unit circuits, each of the plurality of unit circuits including:

- a first transistor having a first terminal, a second terminal, and a first control terminal;
- a second transistor having a third terminal that is coupled to the first control terminal and a fourth terminal that is directly connected to the power source line;
- a capacitive element having a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and
- a third transistor having a fifth terminal that is coupled to the second electrode, a sixth terminal that is coupled to the second signal line, and a third control terminal that is coupled to the first signal line,

a first potential of the first electrode being set to a first voltage by electrically connecting the first electrode and the power source line through the second transistor during a first period, and

a second potential of the first control terminal being set to a second voltage by a data signal that is supplied to the capacitive element through the third transistor during a second period and that changes the first potential from the first voltage by a capacitance coupling involved with the capacitive element.

16. (Previously Presented) The electronic circuit according to claim 15,
each of the plurality of unit circuits further including an electronic element that
is coupled to the first transistor.

17. (Previously Presented) The electronic circuit according to claim 15, further
comprising:

a control circuit that sets a potential of the power source line to a plurality of
potentials or controls an electrical disconnection and an electrical connection between the
power source line and a predetermined voltage.

18. (Previously Presented) The electronic circuit according to claim 15,
each unit circuit of the plurality of unit circuits not including any transistor
other than the first transistor, the second transistor, and the third transistor.

19. (Previously Presented) An electro-optical device, the device comprising:
a plurality of data lines;
a plurality of scanning lines;
a plurality of first power source lines;
a control circuit that sets each of the plurality of first power source lines to a
plurality of potentials or that controls electrical connection and disconnection between each of
the plurality of first power source lines and a predetermined voltage; and
a plurality of unit circuits, each of the plurality of unit circuits including:
a first transistor having a first terminal, a second terminal, and a first
control terminal;
an electro-optical element that is coupled to the first terminal;
a second transistor having a third terminal and a fourth terminal, the
third terminal being coupled to the first control terminal, and the fourth terminal being directly
connected to one first power source line of the plurality of first power source lines;

a capacitive element having a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and

a third transistor having a fifth terminal that is coupled to the second electrode, a sixth terminal that is coupled to one data line of the plurality of data lines, and a third control terminal that is coupled to one scanning line of the plurality of scanning lines, and

an electrically conductive state of the first transistor being determined during at least a part of a period in which the one data line is electrically connected to the second electrode through the third transistor.

20. (Previously Presented) The electro-optical device according to claim 19, further comprising a plurality of second power source lines, and

the second terminal being coupled to one second power source line of the plurality of second power source lines.

21. (Previously Presented) The electro-optical device according to claim 19, the second control terminal being coupled to the third terminal.

22. (Previously Presented) The electro-optical device according to claim 19, the control circuit being a fourth transistor having a seventh terminal and an eighth terminal,

the seventh terminal being coupled to the fourth terminal through one first power source line of the plurality of first power source lines, and

the eighth terminal being coupled to the predetermined voltage.

23. (Previously Presented) The electro-optical device according to claim 19, each unit circuit of the plurality of unit circuits not including any transistor other than the first transistor, the second transistor, and the third transistor.

24. (Previously Presented) The electro-optical device according to claim 19, a conduction type of the first transistor being identical with a conduction type

of the second transistor.

25. (Previously Presented) The electro-optical device according to claim 19, a threshold voltage of the first transistor not being lower than a threshold voltage of the second transistor.

26. (Previously Presented) The electro-optical device according to claim 20, the one second power source line being coupled to the predetermined voltage.

27. (Previously Presented) The electro-optical device according to claim 19, the electro-optical element being an EL element.

28. (Previously Presented) The electro-optical device according to claim 19, the plurality of unit circuits including a group of unit circuits that arranged along one scanning line of the plurality of scanning lines,

the group of unit circuits being used for exhibiting the same color.

29-31 (Canceled)

32. (Previously Presented) An electronic apparatus being equipped with the electronic circuit according to claim 1.

33. (Previously Presented) An electronic apparatus being equipped with the electro-optical device according to claim 19.

34. (Previously Presented) The electro-optical device according to claim 19, the plurality of first power source lines intersecting the plurality of data lines.

35. (Previously Presented) The electro-optical device according to claim 19, the plurality of first power source lines being arranged along the plurality of scanning lines.

36. (Currently Amended) An electronic circuit comprising:
a plurality of unit circuits;
a first power source line; and

a control circuit that sets a potential of the first power source line to a plurality of potentials or controls an electrical disconnection and an electrical connection between the first power source line and a predetermined voltage,

each of the plurality of unit circuits including:

a first transistor having a first terminal, a second ~~transistor terminal~~,

and a first control terminal;

a second transistor having a third terminal, a fourth terminal, and a second control terminal, the third terminal and the fourth terminal being directly connected to the first control terminal and the first power source line, respectively;

a capacitive element having a first electrode and a second electrode, the first electrode being coupled to the first control terminal; and

a third transistor having a fifth terminal and a sixth terminal, the third transistor controlling an electrical connection between a data line and the second electrode.

37. (Previously Presented) An electro-optical device, the device comprising:

a plurality of data lines;

a plurality of scanning lines;

a plurality of first power source lines; and

a plurality of unit circuits, each of the plurality of unit circuits including:

a first transistor having a first terminal, a second terminal, and a first control terminal;

an electro-optical element that is coupled to the first terminal;

a second transistor having a third terminal that is coupled to the first control terminal and a fourth terminal that is directly connected to one first power source line of the plurality of first power source lines;

a capacitive element having a first electrode and a second electrode, the

first electrode being coupled to the first control terminal; and

 a third transistor having a fifth terminal that is coupled to the second electrode, a sixth terminal that is coupled to one data line of the plurality of data lines, and a third control terminal that is coupled to one scanning line of the plurality of scanning lines,

 a first potential of the first electrode being set to a first voltage by electrically connecting the first electrode and the power source line through the second transistor during a first period, and

 a second potential of the first control terminal being set to a second voltage by a data signal that is supplied to the capacitive element through the third transistor during a second period and that changes the first potential from the first voltage by a capacitance coupling involved with the capacitive element.

38. (Previously Presented) The electronic circuit according to claim 6, a driving voltage and a driving current whose levels correspond to the electrically conductive state of the first transistor are supplied to the electronic element during a second period.

39. (Previously Presented) The electronic circuit according to claim 11, the plurality of first power source lines intersecting the plurality of second signal lines.